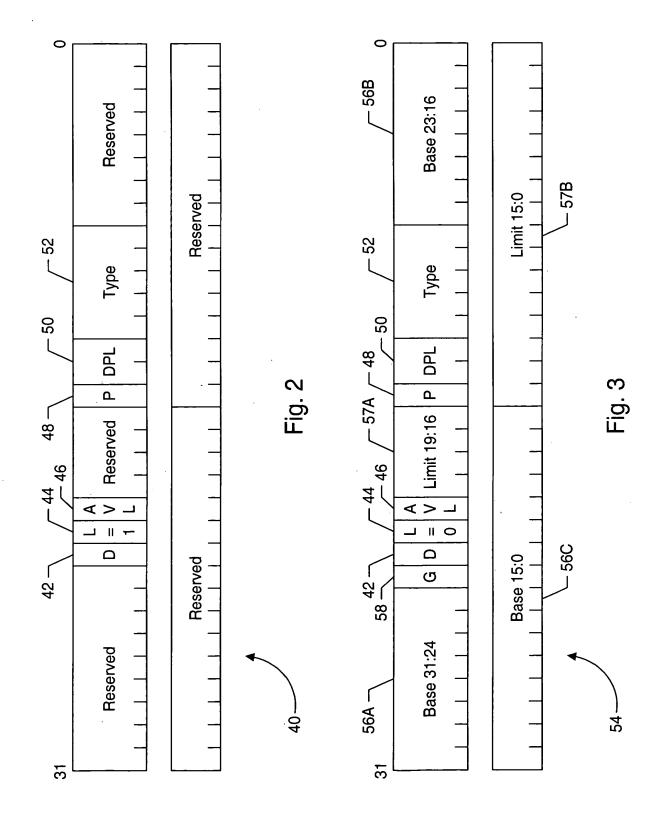


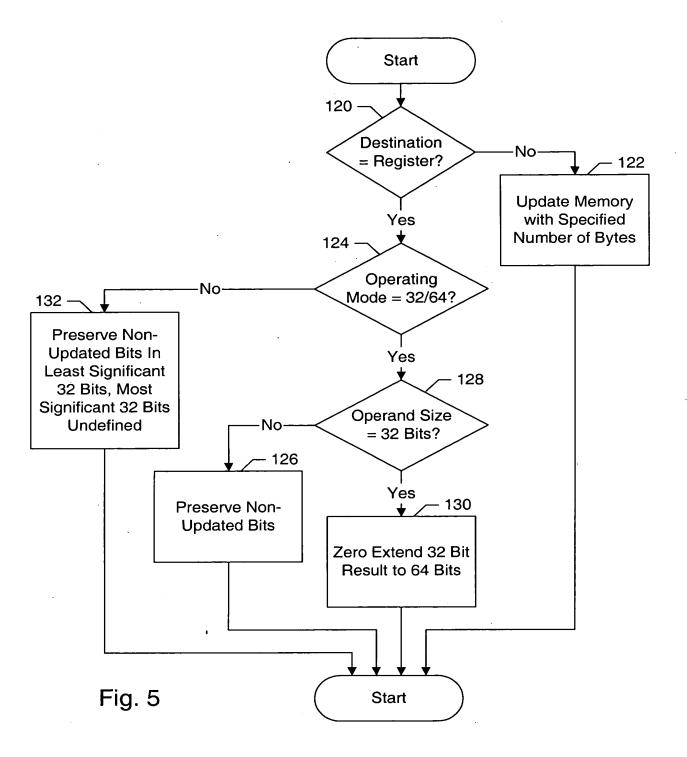
Fig. 1



LME	CS L Bit	CS D Bit	Operating Mode			
· 0	х	0	16 Bit Mode			
0	x	1	32 Bit Mode			
1	0	0	16 Bit Compatibility Mode			
1	0	1	32 Bit Compatibility Mode			
1	1	0	32/64 Mode			
1 .	1	1	Reserved			



Fig. 4



	140	·		142			144			146	
	<b>\</b>		_	<b>♦</b>	_	0	<b>*</b>			<b>*</b>	
8 7 0	Result		8 7 0	Result		8 7 0		·	8 7 0		Fig. 6
16 15			16 15			16 15	Result		16 15		
32 31	Preserve Current Value		32 31	Preserve Current Value		32 31	Zero Extend		32 31	Result	
63			63			63			63		

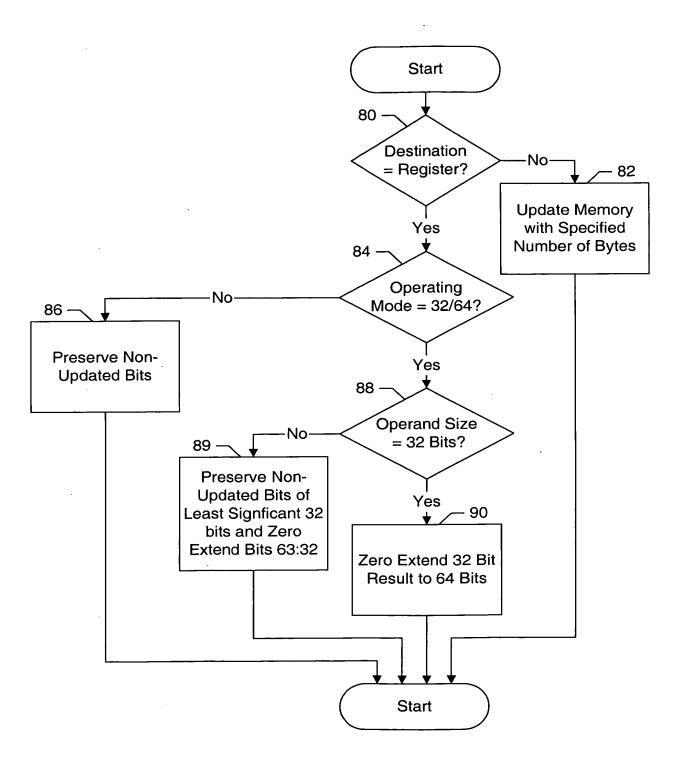
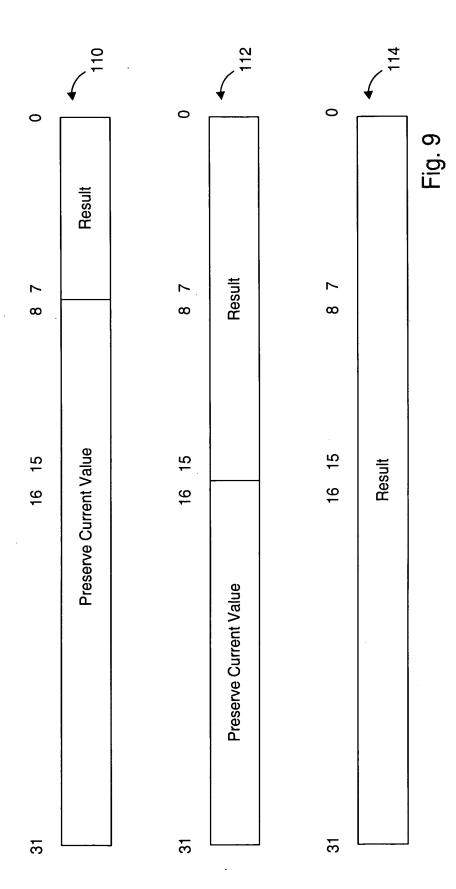
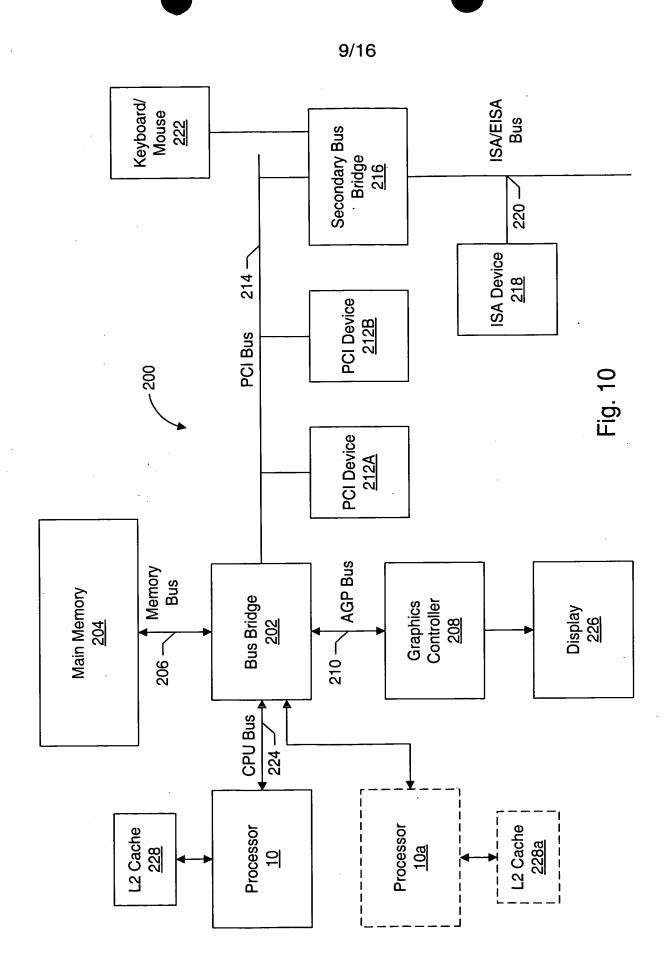


Fig. 7





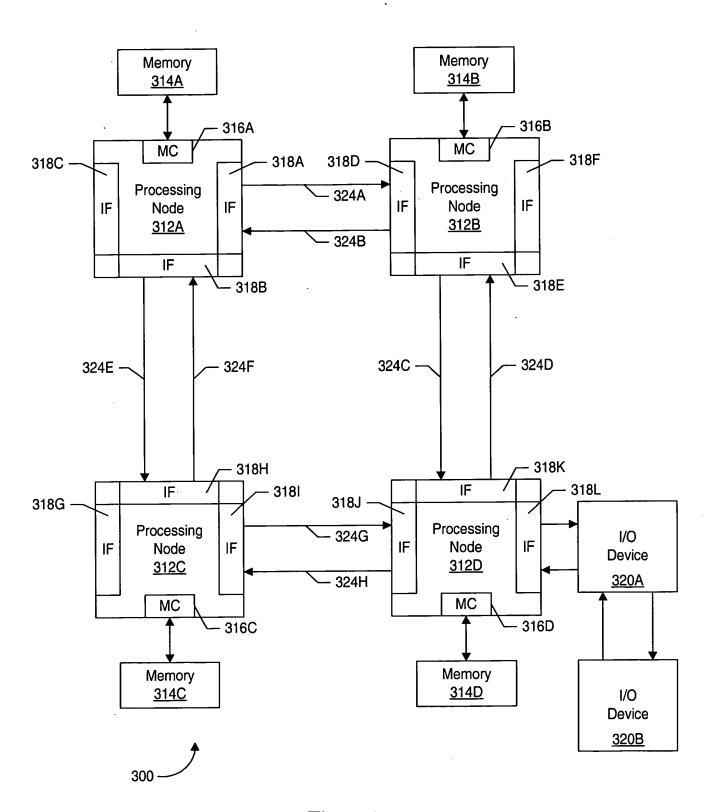


Fig. 11

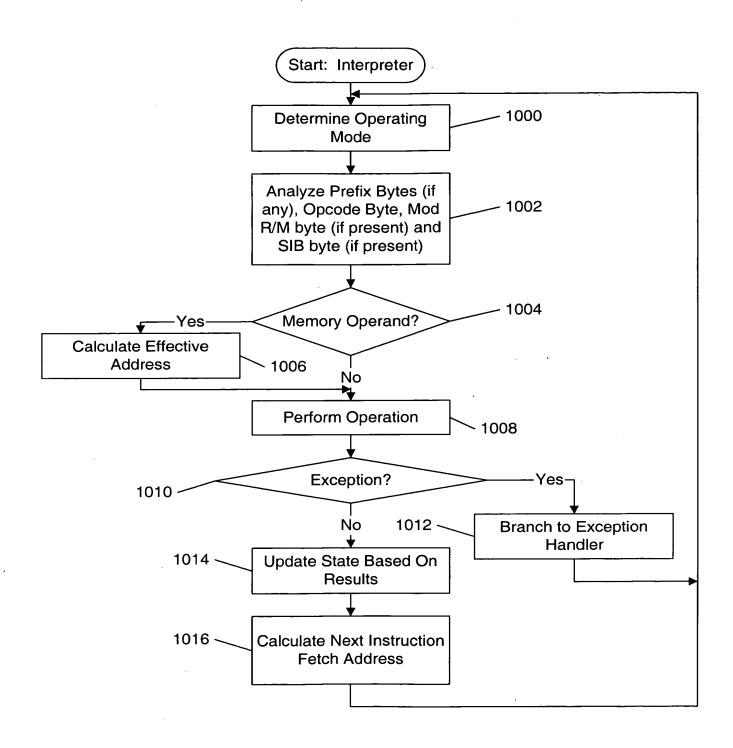


Fig. 12

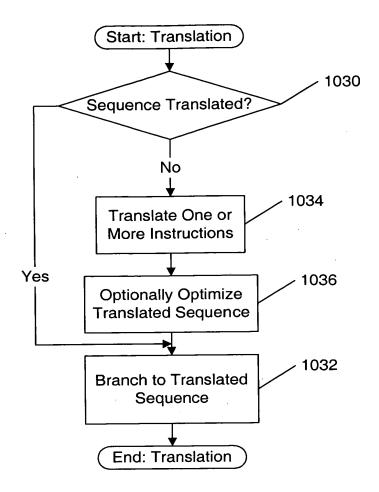


Fig. 13

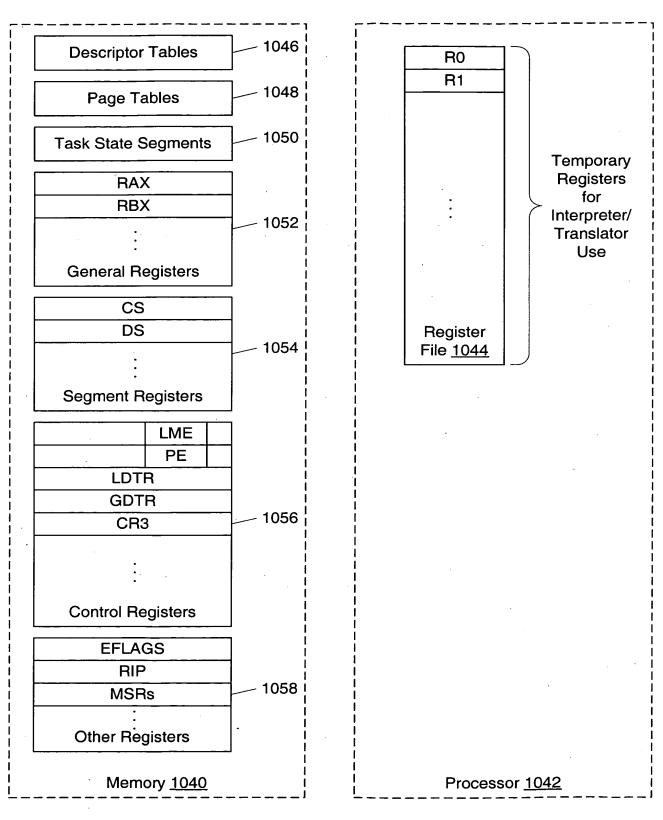


Fig. 14

14

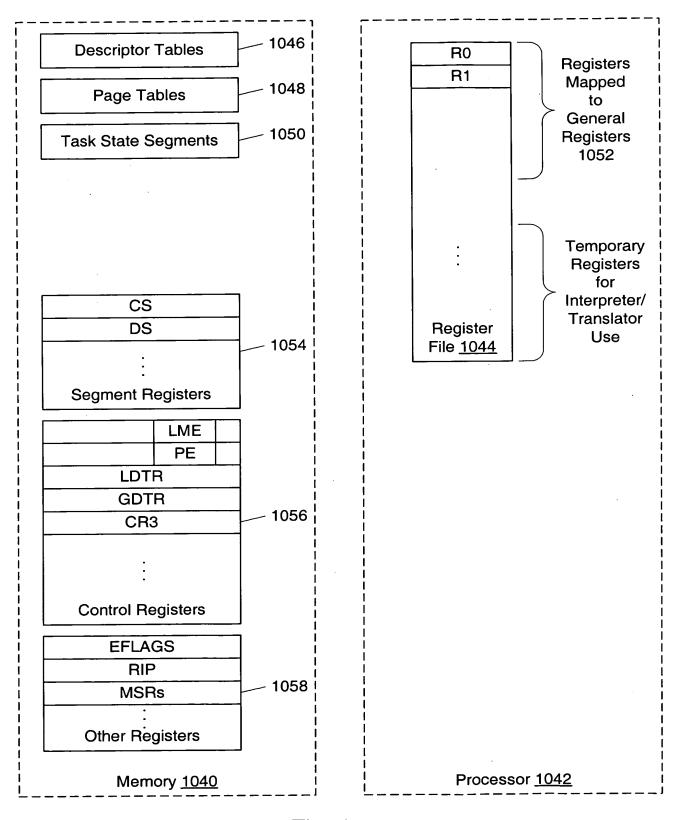


Fig. 15

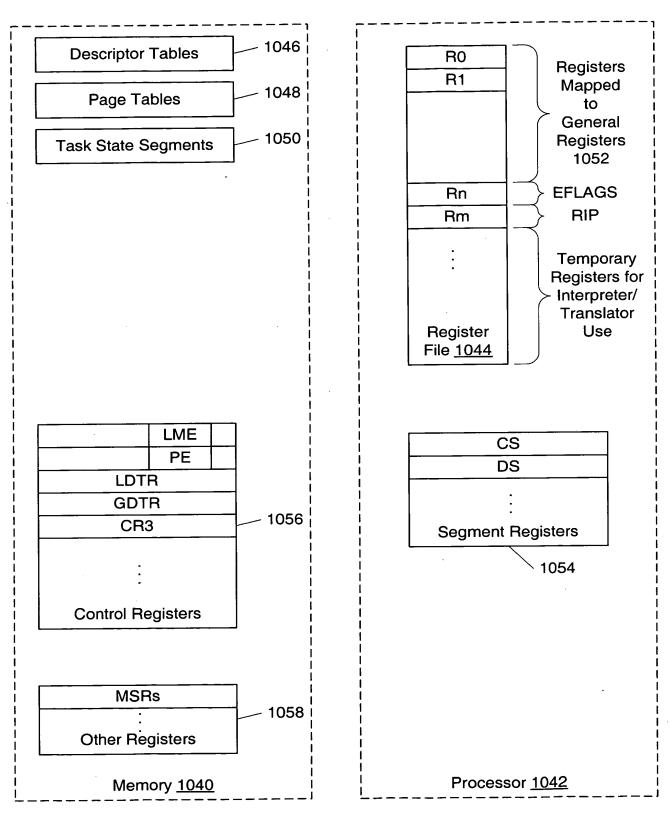


Fig. 16

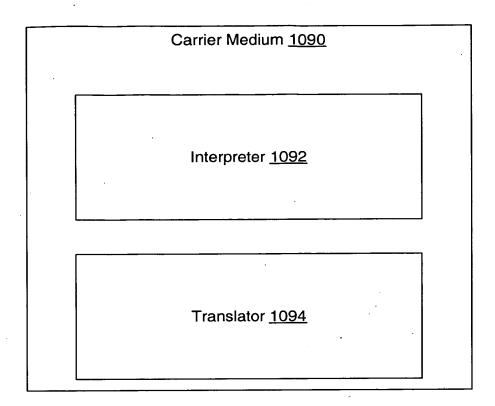


Fig. 17

an at